

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (currently amended) A method performed by a computer system having a hardware emulation, said method comprising the steps of:

obtaining a first set of one or more emulated instructions derived from an original set of one or more instructions using the hardware emulation;

initiating execution of the first set of one or more emulated instructions;

producing first dynamic execution information in response to executing the first set of one or more emulated instructions; and

changing the hardware emulation dynamically for producing a second set of one or more emulated instructions by modifying at least a parameter of one instruction of the first set of one or more emulated instructions in response to said first dynamic execution information, said step of changing, includes software producing multiple conditions codes that replace a single condition code of the first dynamic execution information.

2. (canceled)

3. (previously presented) The method of claim 1, wherein:

said step of changing, includes modifying at least a register field of one instruction of the first set of one or more emulated instructions.

4. (canceled)

5. (previously presented) The method of claim 1, wherein:

said steps of executing, producing and changing are conducted recursively on at least some of successive segments of the first set of one or more emulated instructions.

6. (canceled)

7. (original) The method of claim 1, wherein:

said step of producing, produces branch prediction information; and

said step of changing, changes condition codes of the branch prediction information.

8. (original) The method of claim 1, wherein:

said step of producing, produces a history of register allocation information; and

said step of changing, changes register allocation.

9. (original) The method of claim 1, wherein:

said step of producing, produces a history of branch prediction dynamic execution information; and

said step of changing, generates a branch prediction likelihood code for a group of branches that may be different from any branch prediction of the members of the group.

10-31. (canceled)

32. (previously presented) A method performed by a computer system, said method comprising the steps of:

obtaining an emulated sequence of instructions derived from an original sequence of instructions;

initiating execution of the emulated sequence of instructions;

producing first dynamic execution information in response to executing the emulated sequence of instructions; and

changing the computer system dynamically to produce different dynamic execution information in response to said first dynamic execution information;

wherein said step of changing, includes software producing multiple conditions codes that replace a single condition code of the first dynamic execution information.

33-35. (canceled)

36. (previously presented) The method of claim 1, wherein:

said step of modifying at least a parameter of one instruction of the first set of one or more emulated instructions, includes modifying a plurality of parameters of some instructions of the first set of one or more emulated instructions.

37. (canceled)